



Personal information

First Name and sur Name	Mohammed A. Joudah Al-Ebadi	
Date of Birth	28 Nov. 1978	
Place of Birth	Iraq - Basrah	
Nationality	Iraqi	
Address	Iraq-Basrah-Alkhaleej Alarabi/2	
E-mail	mohammed.joudah@uobasrah.edu.iq	
Scientific Rank	Lecturer	
General Specialty	Computer Engineering	
Precise specialization	Computer Engineering	

the certificates and scientific title

Certificate	title of the thesis	University	Country
Doctorate	FPGA-Based Parallel Shortest Path Searching Processor For OSPF Routing Protocol.	Basrah	Iraq
Master	Parallel Optical Array Arithmetic Using Signed-Digit Number Systems Based On Digit-Decomposition Plane Representation	Basrah	Iraq

training courses

Course Name	Place	Duration	Date
Development of Multimedia Contents	Republic of South Korea	27 day	2016/8/12 – 7/17

Administrative Posts

Career (now)	From	To
Head of computer engineering department	2020	now
Head of exams committee of Department of Computer Engineering	2020	now
Rapporteur of computer engineering department	2014	2020



Lecturer at computer engineering department	2008	now
vice-head of exams committee of Department of Computer Engineering	2014	2020
Head of the attendance committee of students	2014	2020
Member of exams committee of Department of Computer Engineering	2008	now
Member and Head of Many special committees in Engineering and Literature collages	2008	now

Research Activity	
Seminars	Place and date of publication
FPGA Design of Image Encryption and Decryption using Chua's Chaotic Masking	International Journal of Electrical and Computer Engineering, IJECE, Accepted Jul 18, 2021.
Implementation of Chaotic System Using FPGA	2021 6th Asia-Pacific Conference on Intelligent Robot Systems (ACIRS 2021) July 16-18, 2021 – Japan, Tokyo.
New Artificial Neural Network Design for Chua's Chaotic System Prediction Using FPGA Hardware Co-Simulation	International Journal of Electrical and Computer Engineering, IJECE, Accepted May 8, 2021.
Design of High Precision Radix-8 MAF Unit with Reduced Latency	2nd International Congress on Human-Computer Interaction, Optimization and Robotic Applications June 26-27, 2020, Turkey.
FPGA Based Hierarchical Fuzzy System	Proc. of the 2nd International Conference on Electrical, Communication and Computer Engineering (ICECCE) 12-13 June 2020, Istanbul, Turkey



Hardware Implementation for High-Speed Parallel Adder for QSD 2D Data Arrays	Proc. of the 2nd International Conference on Electrical, Communication and Computer Engineering (ICECCE) 12-13 June 2020, Istanbul, Turkey
Fast combined decimal/ binary multiplier based on redundant BCD 4221-8421 digits recoding	Iraqi Journal for Electrical and Electronic Engineering (IJECE) – 2018
A New Hardware Architecture for Fuzzy Logic System Acceleration	Iraqi Journal for Electrical and Electronic Engineering (IJECE) - 2016
Design and Implementation of A Novel FPGA-Based Pipelined-Parallel Processor Architecture for Shortest Path Search.	Proceedings of The Second Engineering Scientific Conference of University of Mosul, College of Engineering - 2013
A New Hardware Architecture for Parallel Shortest Path Searching Processor Based-on FPGA Technology.	International Journal of Electronics and Computer Science Engineering (IJECE) - 2012
Algorithm For 2D-Data Array Addition Using Digit-Decomposition-Plane Representation	Iraqi Journal for Electrical and Electronic Engineering (IJECE) - 2007
High Speed Parallel Optical Adder for Quaternary Signed-Digit Number Using Digit-Decomposition-Plane Representation	J. Basrah Researches (Sciences) - 2006

Cultural activities

Seminars	Place and date
2021 6th Asia-Pacific Conference on Intelligent Robot Systems (ACIRS 2021)	July 16-18, 2021 – Japan, Tokyo.
2nd International Congress on Human-Computer Interaction, Optimization and Robotic Applications	June 26-27, 2020, Turkey.
2nd International Conference on Electrical, Communication and Computer Engineering (ICECCE)	June 12-13 2020, Istanbul, Turkey
Smart City	University of Basrah-College of Engineering, 2017/11/27
Information Security	University of Basrah-College of Engineering, 2017/3/27
Internet of Things (IoT)	University of Basrah-College of Engineering, 2015/12/27
The Second Engineering Scientific Conference of University of Mosul, College of Engineering.	19-21 November 2013.



Awards and thanks and appreciation certificates for the school year

Certificate	data	issued by	reason
Training Course	2016	Republic of South Korea	Attendance of "Development of Multimedia Contents" training course
Certificate of Appreciation	2016	University of ImamJaafar Al-Sadiq	Evaluation of submitted Paper for AIC-MITC Conference
Certificate of Thanks and Appreciation	2015	University of Basrah	For Special Lecturer for 2014-2015 year
Many Thanks certificates	2008 - 2016	University of Basrah	For Member and Head of Exams and other committees

Teaching Experience

studied	Stage
Parallel and Distributed Systems	MsC
Microcontrollers	4 th year
PLC Programming	4 th year
Real Time system Applications	4 th year
FPGA-Based Digital Systems Design	3 rd year
Microprocessors and Microcomputers II	3 rd year
Microprocessors and Microcomputers I	2 nd year
Discrete Structures	2 nd year
Computer Organizations	2 nd year
Fundamentals of Logic Systems	1 st year
Logic Circuits	1 st year
Supervision of postgraduate	number
Master Degree	3